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STATUTORY INSTRUMENTS

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**1989 No. 1100**

**The Design Right (Semiconductor  
Topographies) Regulations 1989**

**Interpretation**

2.—(1) In these Regulations—

“the Act” means the Copyright, Designs and Patents Act 1988(1);

“semiconductor product” means an article the purpose, or one of the purposes, of which is the performance of an electronic function and which consists of two or more layers, at least one of which is composed of semiconducting material and in or upon one or more of which is fixed a pattern appertaining to that or another function; and

“semiconductor topography” means a design within the meaning of section 213(2) of the Act which is a design of either of the following:

- (a) the pattern fixed, or intended to be fixed, in or upon—
  - (i) a layer of a semiconductor product, or
  - (ii) a layer of material in the course of and for the purpose of the manufacture of a semiconductor product, or
- (b) the arrangement of the patterns fixed, or intended to be fixed, in or upon the layers of a semiconductor product in relation to one another.

(2) Except where the context otherwise requires, these Regulations shall be construed as one with Part III of the Act (design right).