
STATUTORY INSTRUMENTS

2006 No. 1833

**The Design Right (Semiconductor
Topographies) (Amendment) Regulations 2006**

Amendment to the Design Right (Semiconductor Topographies) Regulations 1989

3. For regulation 4(2) there shall be substituted—

“(2) Part III of the Act has effect as if for section 217(3) there was substituted the following—

“(3) In this section “qualifying country” means—

- (a) the United Kingdom,
- (b) another member State,
- (c) the Isle of Man, Gibraltar, the Channel Islands or any colony,
- (d) a country listed in the Schedule to the Design Right (Semiconductor Topographies) Regulations 1989.””