

SCHEDULE 1

regulation 4

NEW SCHEDULE FOR THE DESIGN RIGHT  
(SEMICONDUCTOR TOPOGRAPHIES) REGULATIONS 1989

“SCHEDULE

QUALIFYING COUNTRIES

Albania  
Angola  
Antigua and Barbuda  
Argentina  
Armenia  
Australia  
Bahrain, Kingdom of  
Bangladesh  
Barbados  
Belize  
Benin  
Bolivia  
Botswana  
Brazil  
Brunei Darussalam  
Bulgaria  
Burkina Faso  
Burundi  
Cambodia  
Cameroon  
Canada  
Central African Republic  
Chad  
Chile  
China  
Colombia  
Congo  
Costa Rica  
Côte d'Ivoire  
Croatia  
Cuba  
Democratic Republic of the Congo  
Djibouti

**Status:** This is the original version (as it was originally made). This item of legislation is currently only available in its original format.

Dominica  
Dominican Republic  
Ecuador  
Egypt  
El Salvador  
Fiji  
Former Yugoslav Republic of Macedonia  
French overseas territories  
Gabon  
The Gambia  
Georgia  
Ghana  
Grenada  
Guatemala  
Guinea  
Guinea Bissau  
Guyana  
Haiti  
Honduras  
Hong Kong  
Iceland  
India  
Indonesia  
Israel  
Jamaica  
Japan  
Jordan  
Kenya  
Korea, Republic of  
Kuwait  
Kyrgyz Republic  
Lesotho  
Liechtenstein  
Macao, China  
Madagascar  
Malawi  
Malaysia  
Maldives  
Mali  
Mauritania

Mauritius  
Mexico  
Moldova  
Mongolia  
Morocco  
Mozambique  
Myanmar  
Namibia  
Nepal  
Netherlands Antilles  
New Zealand  
Nicaragua  
Niger  
Nigeria  
Norway  
Oman  
Pakistan  
Panama  
Papua New Guinea  
Paraguay  
Peru  
Philippines  
Qatar  
Romania  
Rwanda  
Saint Kitts and Nevis  
Saint Lucia  
Saint Vincent & the Grenadines  
Saudi Arabia  
Senegal  
Sierra Leone  
Singapore  
Solomon Islands  
South Africa  
Sri Lanka  
Suriname  
Swaziland  
Switzerland  
Chinese Taipei  
Tanzania

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Thailand  
 Togo  
 Trinidad and Tobago  
 Tunisia  
 Turkey  
 Uganda  
 United Arab Emirates  
 United States of America  
 Uruguay  
 Venezuela  
 Zambia  
 Zimbabwe”

## SCHEDULE 2

regulation 5

## REVOCATIONS

<i>Title and number</i>	<i>Extent of revocation</i>
The Design Right (Semiconductor Topographies) (Amendment) Regulations 1989 (SI 1989/2147)	The whole Regulations.
The Design Right (Semiconductor Topographies) (Amendment) Regulations 1990 (SI 1990/1003)	The whole Regulations.
The Design Right (Semiconductor Topographies) (Amendment) Regulations 1991 (SI 1991/2237)	The whole Regulations.
The Design Right (Semiconductor Topographies) (Amendment) Regulations 1992 (SI 1992/400)	The whole Regulations.
The Design Right (Semiconductor Topographies) (Amendment) Regulations 1993 (SI 1993/2497)	The whole Regulations.